

**IN THE UNITED STATES DISTRICT COURT
FOR THE WESTERN DISTRICT OF TEXAS
MIDLAND/ODESSA DIVISION**

REDSTONE LOGICS LLC,

Plaintiff,

v.

APPLE INC.,

Defendant.

NO. 7:25-cv-00183-ADA

Jury Trial Demanded

**ORDER GRANTING PARTIES' JOINT MOTION FOR ENTRY
OF PRIOR CLAIM CONSTRUCTION**

The Parties jointly moved for entry of the prior claim construction with regard to certain specified terms. The Parties seek to incorporate into the record of the above-captioned case the claim construction record relevant to certain claim terms in U.S. Patent No. 8,549,339 (the “’339 patent”) that were disputed in *Redstone Logics LLC v. Qualcomm Inc. et al.*, No. 7:24-cv-00231-ADA (the “Qualcomm Case”).

All of the disputed claim terms and proposed constructions in the above-captioned case were presented to the Court in the Qualcomm Case. The Parties each agree not to, and shall not, contend that their Joint Motion and/or entry of relevant portions of the previous claim construction record constitutes a waiver, forfeiture, or lack of preservation of any right to appeal, or of any argument regarding, the instant claim construction order entered in this case.

Having considered the Motion, the Court finds it should be and hereby is **GRANTED**.

1. The Court adopts its reasoning in the Claim Construction Order and Memorandum in Support Thereof from the Qualcomm Case, ECF No. 43, but only insofar as it relates to the following disputed terms, which the Court hereby construes as follows:

| Claim Term in U.S. Patent No. 8,549,339 | Court's Construction |
|---|---|
| "the first clock signal is independent from the second clock signal" (claims 1, 21) | Plain and ordinary meaning, wherein the plain and ordinary meaning does not require that the first and second clock signals depend from different reference oscillator clocks |
| "located in a common region that is substantially central to the first set of processor cores and the second set of processor cores" (claim 14) | Indefinite |

2. The Court adopts the Parties' agreed-upon construction:

| Claim Term in U.S. Patent No. 8,549,339 | Court's Construction |
|--|---|
| "a first/second set of processor cores" (claims 1, 21) | "a first/second group of two or more processor cores" |

3. The Court hereby incorporates the portion of the claim construction record in the Qualcomm Case that the Parties attached to their Joint Motion, including briefing, related declarations, exhibits, preliminary constructions, hearing transcripts, and the Court's October 15, 2025, claim construction order and memorandum, into the above-captioned case.

4. The Court's deadlines for claim construction briefing and the *Markman* hearing scheduled for January 26, 2026, are **VACATED**.

IT IS SO ORDERED.

Signed this 20th day of November, 2025.


 ALAN D ALBRIGHT
 UNITED STATES DISTRICT JUDGE